

100V,228A N-CHANNEL POWER MOSFET

GENERAL DESCRIPTION

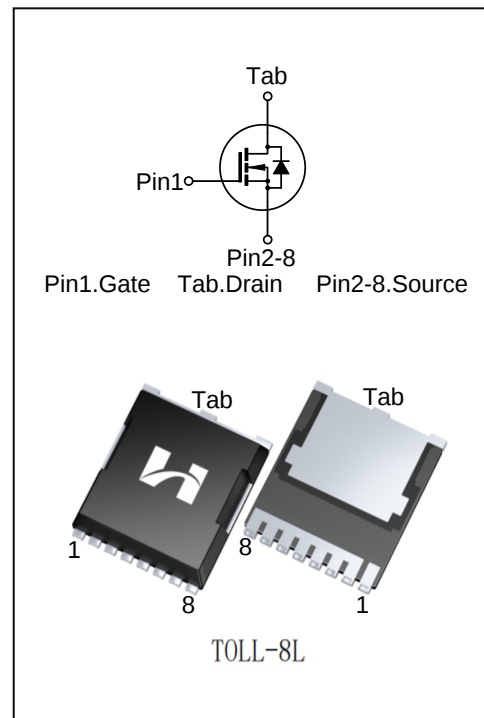
The SGL102R3T uses advanced SGT technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features

- ◆ $V_{DS}=100V, I_D=228A$
- ◆ $R_{DS(on)}$
TYP: $1.6m\Omega @ V_{GS}=10V$

Applications

- ◆ Power factor correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SGL102R3T	TOLL-8L	SGL102R3T	Pb Free	Reel

ABSOLUTE MAXIMUM RATINGS ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Characteristics	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current	I_D	$T_C = 25^{\circ}\text{C}$	A
		$T_C = 100^{\circ}\text{C}$	
Drain Current Pulsed (Note 1)	I_{DM}	912	A
Power Dissipation($T_C=25^{\circ}\text{C}$) -Derate above 25°C	P_D	284	W
		1.89	W/ $^{\circ}\text{C}$
Single Pulsed Avalanche Energy (Note 2)	E_{AS}	2592	mJ
Operation Junction Temperature Range	T_J	$-55\sim+150$	$^{\circ}\text{C}$
Storage Temperature Range	T_{stg}	$-55\sim+150$	$^{\circ}\text{C}$
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	TL	300	$^{\circ}\text{C}$

THERMAL CHARACTERISTICS

Characteristics	Symbol	Ratings	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.44	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	45	$^{\circ}\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	B _V DSS	V _{GS} =0V, I _D =250μA	100	--	--	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V	--	--	100	nA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =20V, V _{DS} =0V	--	--	100	nA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =-20V, V _{DS} =0V	--	--	-100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS} , I _D =250μA	2.5	--	4.5	V
Static Drain- Source On State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =60A	--	1.6	2.3	mΩ
Dynamic Characteristics						
Gate Resistance	R _g	V _{GS} =0V; f=1.0MHZ	--	2.7	--	Ω
Input Capacitance	C _{iss}	V _{DS} =50V	--	13656	--	pF
Output Capacitance	C _{oss}	V _{GS} =0V	--	2059	--	
Reverse Transfer Capacitance	C _{rss}	f=1.0MHZ	--	65	--	
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	V _{DD} =50V V _{GS} =10V R _G =4.7Ω I _D =90A	--	59	--	ns
Turn-on Rise Time	t _r		--	97	--	
Turn-off Delay Time	t _{d(off)}		--	133	--	
Turn-off Fall Time	t _f		--	85	--	ns
Total Gate Charge	Q _g	V _{DD} =50V, I _D =90A V _{GS} =10V	--	184	--	nC
Gate-Source Charge	Q _{gs}		--	75	--	
Gate-Drain Charge	Q _{gd}		--	32	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_S	Integral Reverse P-N Junction Diode in the MOSFET	--	--	228	A
Pulsed Source Current	I_{SM}		--	--	912	
Diode Forward Voltage	V_{SD}	$I_S=60A, V_{GS}=0V$	--		1.4	V
Reverse Recovery Time	T_{rr}	$I_S=90A, V_R=80V, dI_F/dt=100A/\mu S$	--	91	--	ns
Reverse Recovery Charge	Q_{rr}		--	238	--	μC

1. Pulse width limited by maximum junction temperature
2. $L=1.0mH, V_{DD}=80V, V_G=10V, R_G=25\Omega$, starting $T_J=25^\circ C$
3. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
4. Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. Output Characteristics

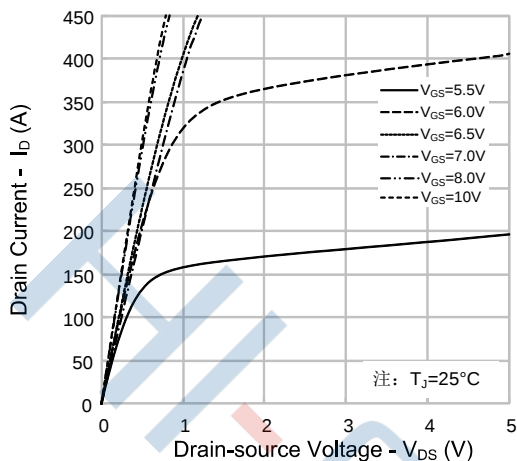


Figure 2. Transfer Characteristics

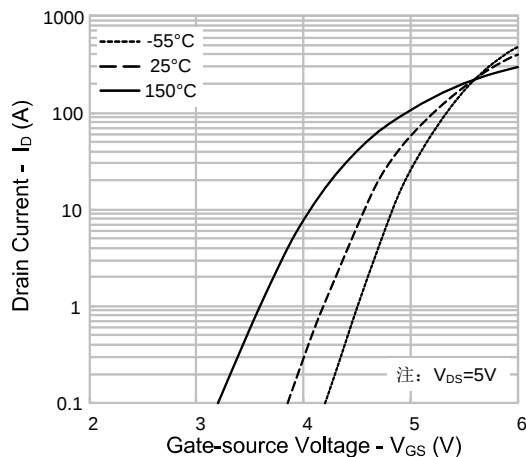


Figure 3. On-resistance vs. Drain Current

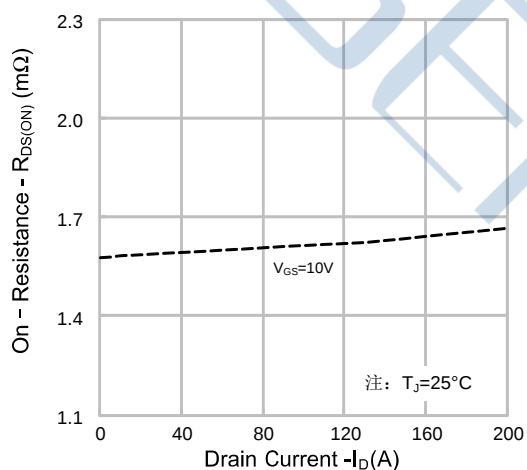


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

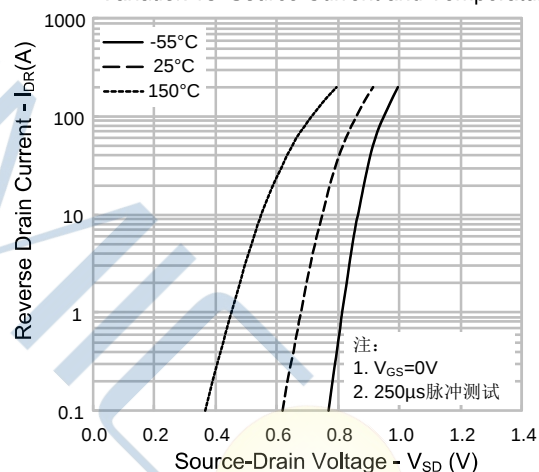


Figure 5. Capacitance Characteristics

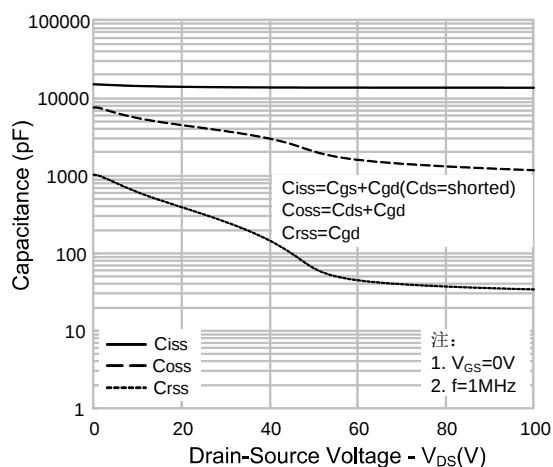
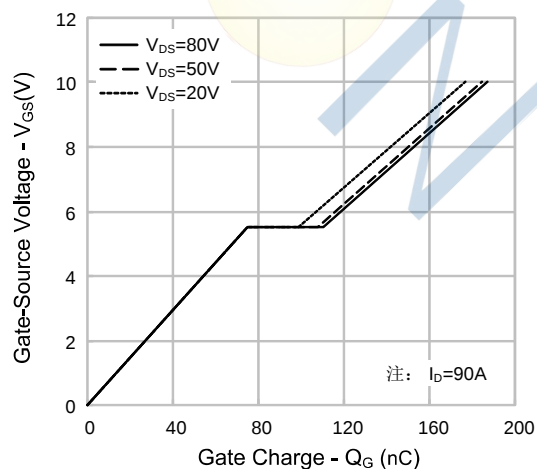


Figure 6. Gate Charge



Typical Performance Characteristics

Figure 7. Breakdown Voltage vs. Temperature Characteristics

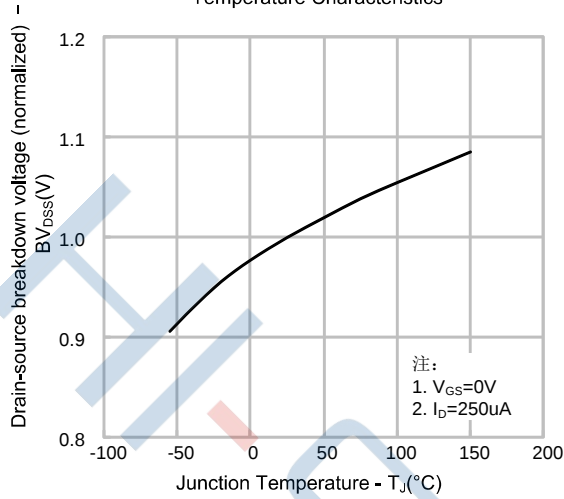


Figure 8. On-resistance vs. Temperature Characteristics

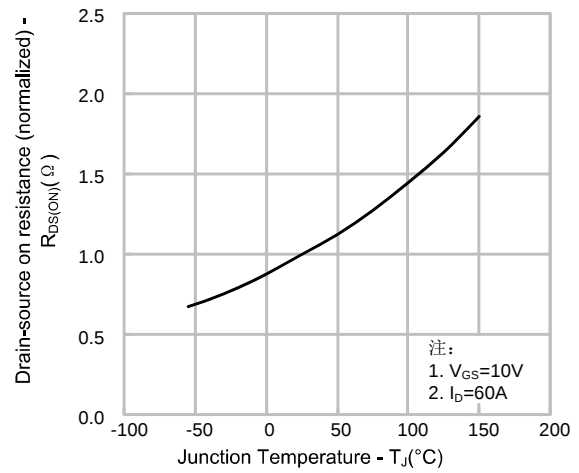
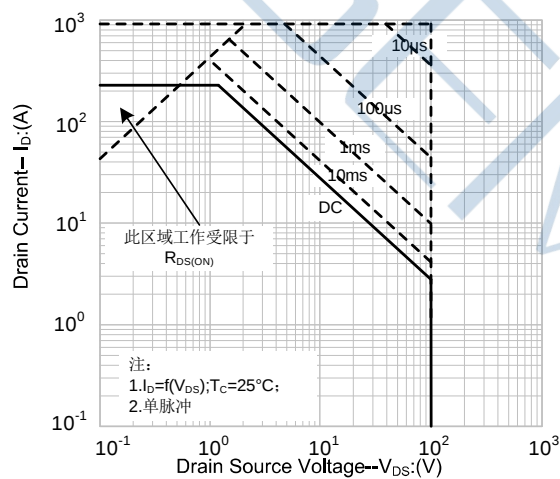
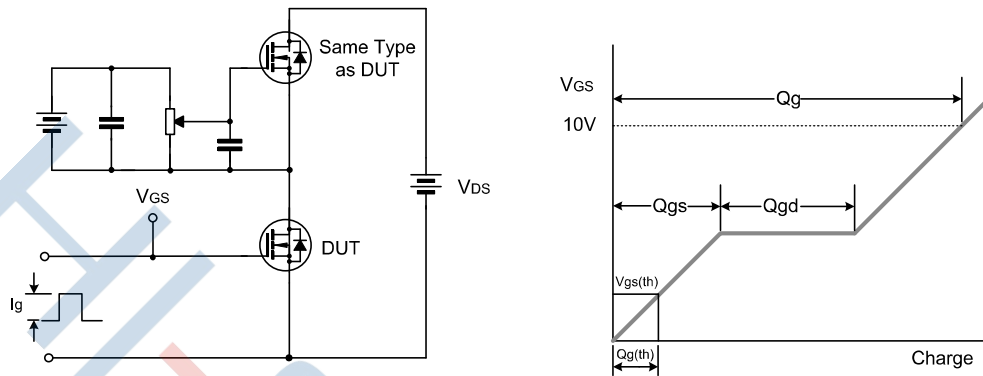


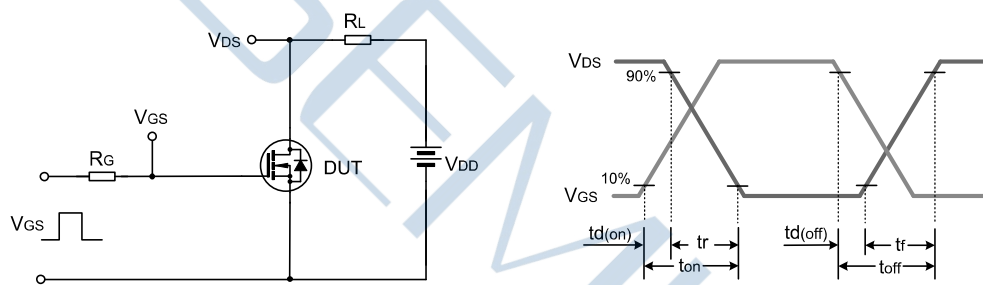
Figure 9. Max. Safe Operating



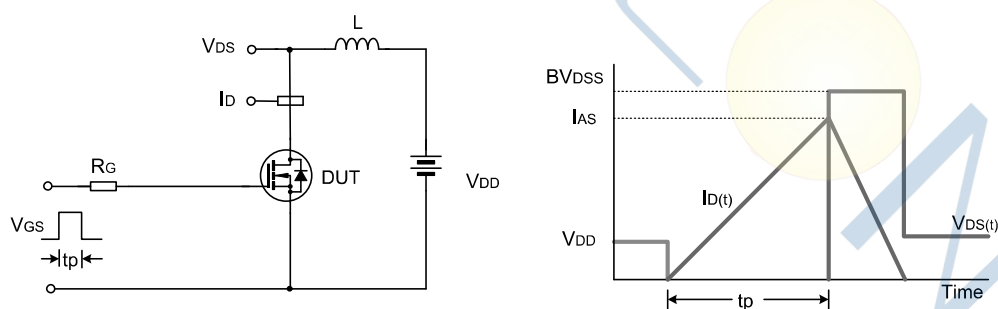
Test Circuit



Gate Charge Test Circuit & Waveform

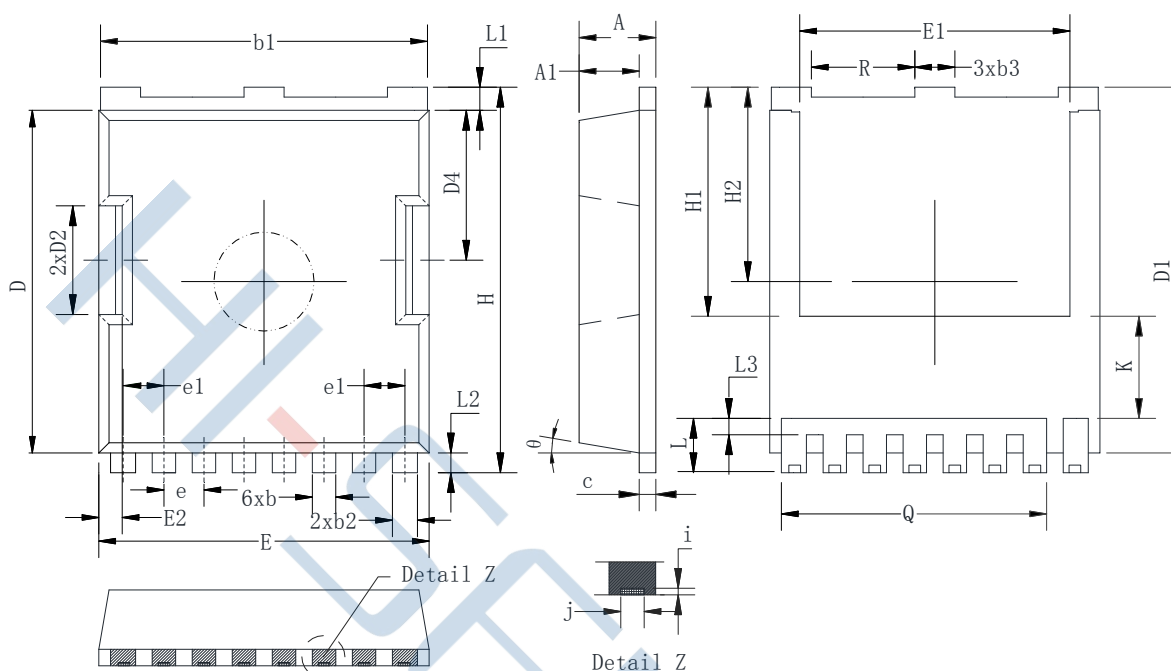


Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform

Package Dimensions of TOLL-8L



Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.25	2.30	2.35	E2	0.65	0.70	0.75
A1	1.75	1.80	1.85	H	11.60	11.70	11.80
b	0.65	0.70	0.75	H1	6.95 BSC		
b1	9.75	9.80	9.85	H2	5.90 BSC		
b2	0.70	0.75	0.80	i	0.10 REF		
b3	1.15	1.20	1.25	j	0.35 REF		
c	0.45	0.50	0.55	K	3.10 REF		
D	10.	10.40	10.	L	1.55	1.65	1.75
D1	3511.	11.10	4511.	L1	0.65	0.70	0.75
D2	00	3.30	20	L2	0.50	0.60	0.70
D4	3.25	4.55	3.35	L3	0.40	0.50	0.60
e	4.50	1.20 BSC	4.60	Q	7.95 REF		
e1	1.225 BSC			R	3.05	3.10	3.15
E	9.85	9.90	9.95	θ	10°REF		
E1	8.00	8.10	8.20				

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